

## **Magma Customers Pass 50-Tapeout Mark at 45/40 Nanometers Using Talus**

*More than with any Other EDA Platform - Talus 1.1 Proven as Leading Implementation Solution for Advanced Chips*

Bangalore, Karnataka, August 27, 2009 /[India PRwire](#)/ -- Magma® Design Automation Inc. (Nasdaq: LAVA), a provider of chip design solutions, today announced Magma customers have passed the 50-tapeout mark for chips designed at 45 nanometer (nm) or smaller geometries using Magma's Talus® netlist-to-GDSII design implementation system – more than with any other EDA supplier's implementation system. Talus, Magma's next-generation implementation platform designed specifically for chips at 45-/40-nm or smaller process nodes, is now widely used among Magma customers and its latest release, Talus 1.1, has demonstrated particular advantages for designs at the 45-/40-nm process nodes.

More than 55 percent of the 45- and 40-nm tapeouts were completed for networking and mobile applications. Other applications taking advantage of 45- and 40-nm technology include multimedia and graphics. In terms of geographic distribution, about 70 percent of the 45- and 40-nm tapeouts completed to date were by companies based in North America and about 25 percent by companies based in Japan or the Asia-Pacific region.

“As you might expect, networking and mobile applications represent the bulk of chips completed at 45 or 40 nm,” said Premal Buch, general manager of Magma's Design Implementation Business Unit. “The designs completed so far at these geometries tend to be complex, in some cases approaching 100 million gates. Talus 1.1 with its COre™ (concurrent optimizing routing engine) and high capacity is ideally suited for implementing chips in these application areas which tend to push the performance envelope as well as have high gate counts.”

### **Talus 1.1: The Fastest Path to Silicon for 45-/40-nm Chips**

The Talus system was built to anticipate the unique requirements of chip design at advanced process nodes, and Talus 1.1 takes its capabilities even further. Since its availability was announced in May 2009, Magma customers have found Talus 1.1 to deliver significant improvements in runtime and timing convergence. It also achieves timing closure with no design-rule checking (DRC) violations and reduces total chip area significantly. Talus also offers a significant capacity advantage over competing systems which allows design teams to work on much larger blocks during the design process.

“Magma's raison d'être from our beginning has been to provide designers with the best technology for advanced chips,” Buch added. “That's why we closely track how many chips are taped out as the semiconductor community transitions to new process geometries. The transition to the 45-/40-nm nodes has again created an opportunity for Magma to expand market share. Of course, we are not stopping there and already have the foundation in place to support the next process node at 32/28nm. In fact, we are already seeing some of our customers working on 28-nm designs.”

### **Notes to Editor**

#### About Magma

Magma's electronic design automation (EDA) software is used to create complex, high-performance integrated circuits (ICs) for cellular telephones, electronic games, WiFi, MP3 players, DVD/digital video, networking, automotive electronics and other electronic applications. Magma products for IC implementation, analog/mixed-signal design, analysis, physical verification, circuit simulation and characterization are recognized as embodying the best in semiconductor technology, providing the world's top chip companies the "Fastest Path to Silicon."™ Magma maintains headquarters in San Jose, Calif., and offices throughout North America, Europe, Japan, Asia and India. Magma's stock trades on Nasdaq under the ticker symbol LAVA. Visit Magma Design Automation on the Web at [www.magma-da.com](http://www.magma-da.com).

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**Forward-Looking Statements:**

Except for the historical information contained herein, the matters set forth in this press release, including statements that Talus has been used to implement more 45-/40-nm designs than other implementation systems, offers faster runtime and timing convergence, reduces area and provides higher capacity of advanced designs and other statements about the features and benefits of Magma's products, are forward-looking statements within the meaning of the "safe harbor" provisions of the Private Securities Litigation Reform Act of 1995. These forward-looking statements are subject to risks and uncertainties that could cause actual results to differ materially, including, but not limited to, the ability of Magma's products to produce the desired results and Magma's ability to keep pace with rapidly changing technology. Further discussion of these and other potential risk factors may be found in Magma's public filings with the Securities and Exchange Commission ([www.sec.gov](http://www.sec.gov)). Magma undertakes no additional obligation to update these forward-looking statements.

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